Call for papers

- **Objective**
  - Widely publish research results on System LSI Design Methodology

- **About the Transactions**
  - Issued twice a year on February and August
  - Submission deadlines are June and December, respectively
    - (Next deadline is December 4, 2019 (hard deadline))
  - All papers are available for free on the Web: [http://www.jstage.jst.go.jp/browse/ipsjtssl](http://www.jstage.jst.go.jp/browse/ipsjtssl)
  - Papers are registered in many databases: INSPEC, EI, DBLP, SwetsWise, SciVerse Scopus, CrossRef, Google Scholar, Microsoft Academic Search, JDreamII
  - Short paper category suitable for preliminary publication
  - A paper with novelty OR useful information is considered for short paper publication.
  - A complete version can be submitted later as a regular paper.
  - A short paper is limited to 2 pages except for acknowledgement and references, and the revised version is within a half page more.
  - Invited papers by world-famous researchers
    - 23rd issue (August 2019)
      - Chaofei Yang, Ximing Qiao, Yiran Chen (Duke University), “Neuromorphic Computing Systems: From CMOS To Emerging Nonvolatile Memory”
    - 22nd issue (February 2019)
    - 21st issue (August 2018)
      - Melanie Diepenbeck (University of Bremen), Ulrich Kühne (Université Paris-Saclay), Mathias Soeken (EPFL), Daniel Grosse (University of Bremen, DFKI Bremen), Rolf Drechsler (University of Bremen), “Behaviour Driven Development for Hardware Design”
    - 20th issue (February 2018)
      - Bing Li (Technical University of Munich), Masanori Hashimoto (Osaka University), Ulf Schlichtmann (Technical University of Munich), “From Process Variations to Reliability: A Survey of Timing of Digital Circuits in the Nanometer Era”
    - 19th issue (August 2017)
      - Johann Knechtel, Ozgur Sinanoglu (NYU Abu Dhabi), Ibrahim (Abe) M. Elfadel (Masdar Institute, Khalifa Univ. of Science and Technology), Jens Lienig (TU Dresden), and Cliff C. N. Sze (Google Inc.), “Large-Scale 3D Chips: Challenges and Solutions for Design Automation, Testing, and Trustworthy Integration”

- TSLDM Best Paper Award honors the authors of distinguished paper

- For more details, please visit [http://www.sig-sldm.org/tsldm/](http://www.sig-sldm.org/tsldm/)