

## Call for papers

# IPSI Transactions on System LSI Design Methodology

### ➤ Objective

- ✓ Widely publish research results on System LSI Design Methodology

### ➤ About the Transactions

- ✓ Issued twice a year on February and August
- ✓ Submission deadlines are June and December, respectively  
(Next deadline is June 1, 2018 (hard deadline))
- ✓ All papers are available for free on the Web  
<http://www.jstage.jst.go.jp/browse/ipsjtsldm>
- ✓ Papers are registered in many databases *INSPEC, EI, DBLP, SwetsWise, SciVerse Scopus, CrossRef, Google Scholar, Microsoft Academic Search, JDreamII*
- ✓ Short paper category suitable for preliminary publication



A paper with novelty OR useful information is considered for short paper publication. A complete version can be submitted later as a regular paper.

[New!] A Short Paper is limited to 2 pages, and the revised version is within a half page more.

- ✓ Invited papers by **world-famous researchers**

20<sup>th</sup> issue (February 2018)

Bing Li (Technical University of Munich), Masanori Hashimoto (Osaka University), Ulf Schlichtmann (Technical University of Munich), *"From Process Variations to Reliability: A Survey of Timing of Digital Circuits in the Nanometer Era"*

19<sup>th</sup> issue (August 2017)

Johann Knechtel, Ozgur Sinanoglu (NYU Abu Dhabi), Ibrahim (Abe) M. Elfadel (Masdar Institute, Khalifa Univ. of Science and Technology), Jens Lienig (TU Dresden), and Cliff C. N. Sze (Google Inc.), *"Large-Scale 3D Chips: Challenges and Solutions for Design Automation, Testing, and Trustworthy Integration"*

18<sup>th</sup> issue (February 2017)

Xiaoqing Xu, David Z. Pan (Univ. of Texas at Austin), *"Toward Unidirectional Routing Closure in Advanced Technology Nodes"*

17<sup>th</sup> issue (August 2016)

Amro Awad, Ganesh Balakrishnan, Yipeng Wang, and Yan Solihin (North Carolina State Univ.), *"Accurate Cloning of the Memory Access Behavior"*

16<sup>th</sup> issue (February 2016)

Wing-Kai Chow, Evangeline F.Y. Young (Chinese Univ. of Hong Kong), *"Placement: From Wirelength to Detailed Routability"*

- ✓ TSLDM Best Paper Award honors the authors of distinguished paper

➤ For more details, please visit <http://www.sig-sldm.org/tsldm/>